

Figure 1 (Prior art)

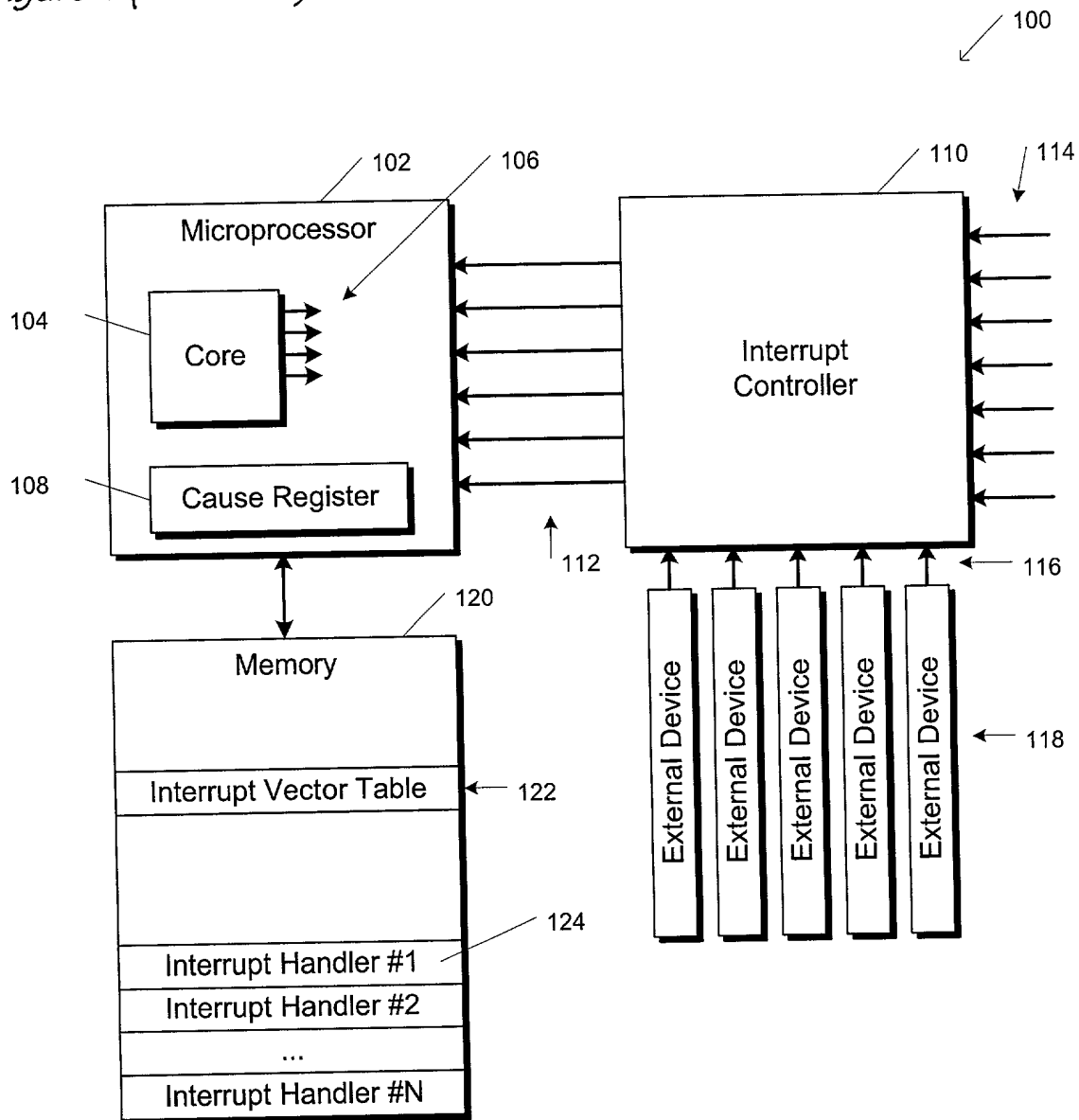


Figure 2 (Prior Art)

Flow Chart for Prior Art Interrupt Handling

200

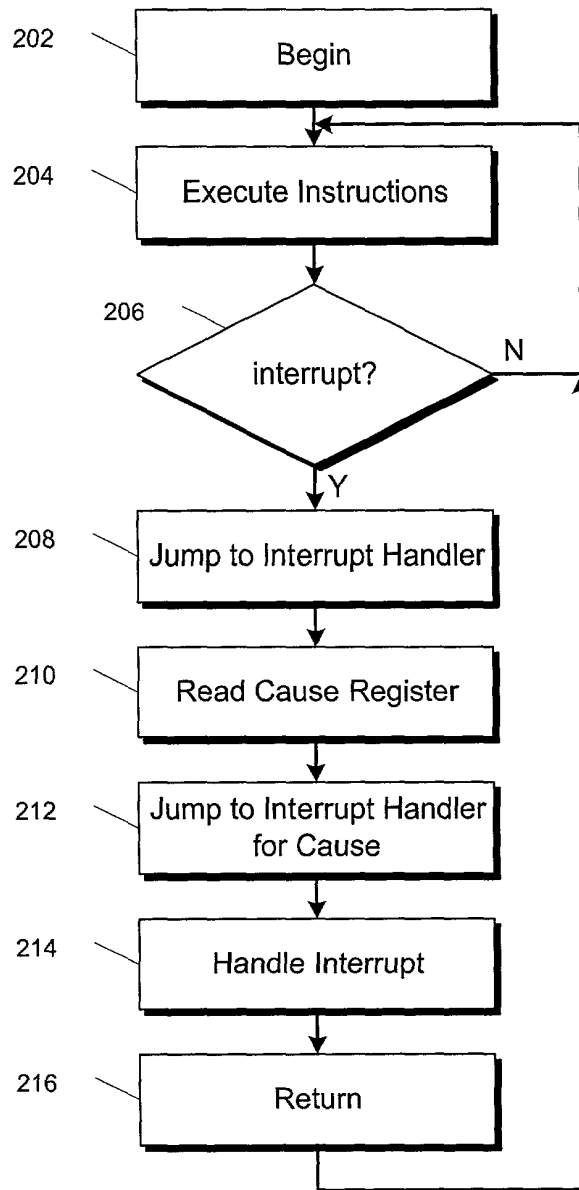


Figure 3

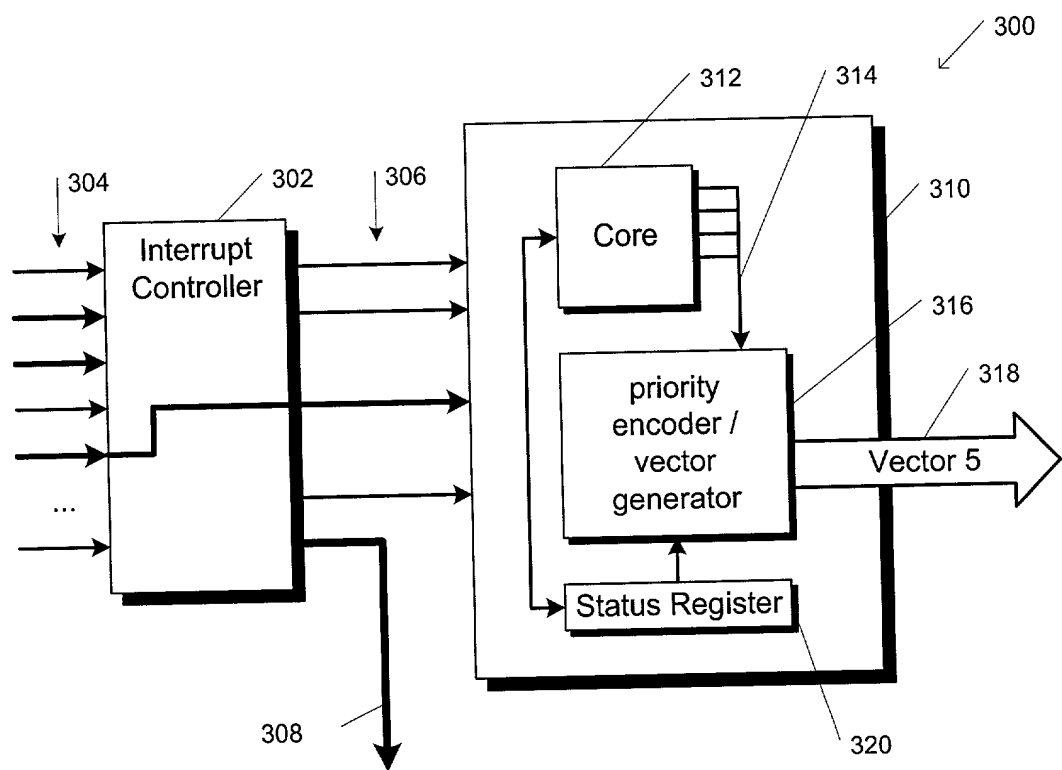


Figure 5

Configurable Priority Levels

bit field value	priority level	bit field value	priority level
0000	Level 0.5 - Highest	1000	Level 4.5
0001	Level 1.0	1001	Level 5
0010	Level 1.5	1010	Level 5.5
0011	Level 2	1011	Level 6
0100	Level 2.5	1100	Level 6.5
0101	Level 3	1101	Level 7
0110	Level 3.5	1110	Level 7.5
0111	Level 4	1111	Level 8

Figure 4

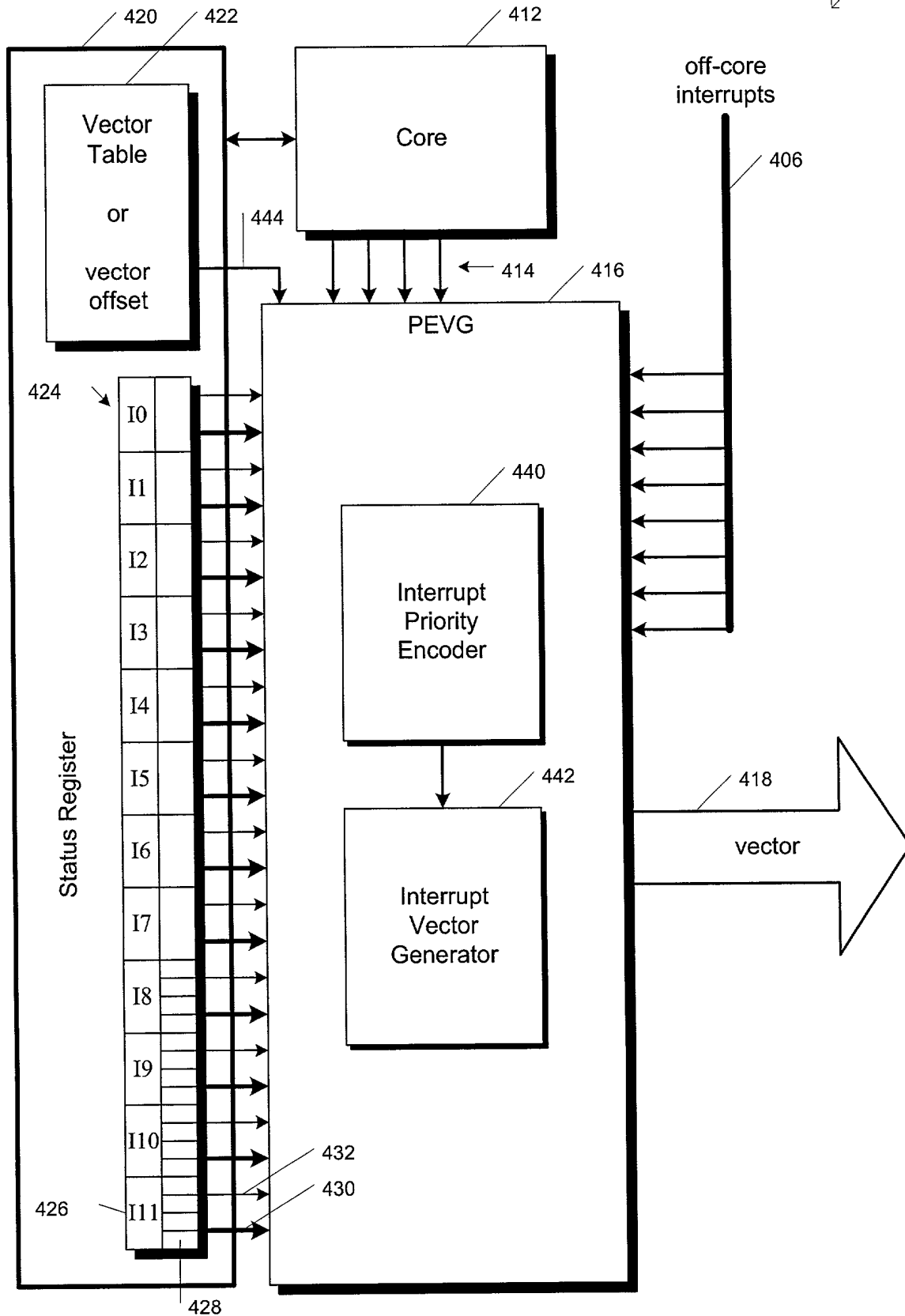


Figure 6

